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Enrolment No:	

UNIVERSITY OF PETROLEUM AND ENERGY STUDIES
Online End Semester Examination, Dec 2020

Course: Operating System Concepts Program: B. Tech. CSE-IBM + Xebia Specializations Course Code: CSEG-2007	Semester: III Time 03 hrs. Max. Marks: 100
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SECTION A

1. Each Question will carry 5 Marks
2. Instruction: Complete the statement / Select the correct answer(s)

S. No.	Question	CO
Q 1	Analyze the necessary conditions that a solution to critical section should meet?	CO2
Q2	a) How Paging manages to non-contiguous allocation of processes? b) How Paging with TLB is efficient than paging scheme?	CO4
Q3	Simulate the protection mechanism of Operating System in context of Access Control Matrix.	CO5
Q4	Differentiate between SCAN and CSCAN Algorithm. Which will have lesser seek time?	CO3
Q5	Write down page fault handling steps.	CO5
Q6	a) What are the components of a PCB block? b) Write about scheduler which monitors degree of multiprogramming in system.	CO1

SECTION B

1. Each question will carry 10 marks
2. Instruction: Write short / brief notes

Q 7	Compile the completion time for the following processes when the scheduling algorithm is SRTF and each process first spends some time on I/O, then on CPU and again on I/O. Assume that there are multiple I/O devices.	CO1																							
	<table border="1" style="width: 100%; border-collapse: collapse;"> <thead> <tr> <th rowspan="2">Process</th> <th rowspan="2">Arrival Time</th> <th colspan="3">Execution Time</th> </tr> <tr> <th>I/O Time</th> <th>CPU Time</th> <th>I/O Time</th> </tr> </thead> <tbody> <tr> <td>P1</td> <td>0</td> <td>4</td> <td>14</td> <td>2</td> </tr> <tr> <td>P2</td> <td>1</td> <td>8</td> <td>28</td> <td>4</td> </tr> <tr> <td>P3</td> <td>3</td> <td>12</td> <td>42</td> <td>6</td> </tr> </tbody> </table>	Process	Arrival Time	Execution Time			I/O Time	CPU Time	I/O Time	P1	0	4	14	2	P2	1	8	28	4	P3	3	12	42	6	
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Q 8	How many total processes are created if parent is executing two fork system calls as fork(); fork(); Draw its activation tree also.	CO2
Q 9	Design solution to Critical Section Problem for 2-process system.	CO3
Q 10	a) Compare and Contrast between timesharing and real time systems. b) What is Belady's anomaly? Discuss in context of any page replacement algorithm.	CO4
Q 11	Design solution for classical synchronization problem "Dinning Philosophers Problem", with supportive brief explanation.	CO5

Section C

1. Each Question carries 20 Marks.

2. Instruction: Write long answer.

Q12	<p>: Consider the following snapshot of a system</p> <table style="margin-left: 40px;"> <thead> <tr> <th><i>Process</i></th> <th><i>Allocation</i></th> <th><i>Max</i></th> <th><i>need</i></th> <th><i>Available</i></th> </tr> <tr> <th></th> <th>A B C</th> <th>A B C</th> <th>A B C</th> <th>A B C</th> </tr> </thead> <tbody> <tr> <td><i>P0</i></td> <td>1 1 2</td> <td>4 3 3</td> <td></td> <td>2 1 0</td> </tr> <tr> <td><i>P1</i></td> <td>2 1 2</td> <td>3 2 2</td> <td></td> <td></td> </tr> <tr> <td><i>P2</i></td> <td>4 0 1</td> <td>9 0 2</td> <td></td> <td></td> </tr> <tr> <td><i>P3</i></td> <td>0 2 0</td> <td>7 5 3</td> <td></td> <td></td> </tr> <tr> <td><i>P4</i></td> <td>1 1 2</td> <td>10 2 3</td> <td></td> <td></td> </tr> </tbody> </table> <p>Answer the following questions using the banker's algorithm:</p> <ol style="list-style-type: none"> Determine the total number of resources present of each type (A,B,C) in the system. What is the content of the matrix Need? Is the system in a safe state? If a request from process <i>P1</i> arrives for (2,1,0), can the request be granted? <p style="text-align: center;">OR</p> <p>Comply with Paging Memory Management Technique with Suitable diagram. Consider a paging system with the page table stored in memory.</p> <ol style="list-style-type: none"> If a memory reference takes 200 nanoseconds, how long does a paged memory reference take? If we add associative registers, and 75 percent of all page-table references are found in the associative registers, what is the effective memory reference time? (Assume that finding a page-table entry in the associative registers takes zero time, if the entry is there.) 	<i>Process</i>	<i>Allocation</i>	<i>Max</i>	<i>need</i>	<i>Available</i>		A B C	A B C	A B C	A B C	<i>P0</i>	1 1 2	4 3 3		2 1 0	<i>P1</i>	2 1 2	3 2 2			<i>P2</i>	4 0 1	9 0 2			<i>P3</i>	0 2 0	7 5 3			<i>P4</i>	1 1 2	10 2 3			CO3
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